AMENDMENTS TO THE CLAIMS:

This listing of claims will replace all prior versions, and listings, of claims in the application:

LISTING OF CLAIMS:

Claims 1-3 (Canceled).

4. (New) A data processing device, comprising:

an array of data processing units, the data processing units being connected to at least one of a power supply line and a clock line; and

an enabling/disabling element configured to at least one of: i) enable or disable power supply to a number of the data processing units, and ii) block full clock speed for the number of data processing units;

wherein the number is less than all of the data processing units.

5. (New) The data processing device according to claim 4, wherein the enabling/disabling element is adapted to provide a clock to a number of the data processing units which is equal to 0.

6. (New) The data processing device according to claim 4, wherein the enabling/disabling device is adapted to be handshake-driven.

7. (New) The data processing device according to claim 4, wherein each of the data processing units is a reconfigurable unit of a multi-dimensional array.

8. (New) The data processing device according to claim 4, wherein the enabling/disabling element is configured to selectively enable or disable the power supply to the number of data processing units.

9. (New) The data processing device according to claim 4, wherein the enabling/disabling element is configured to selectively block full clock speed for the number of data processing units.